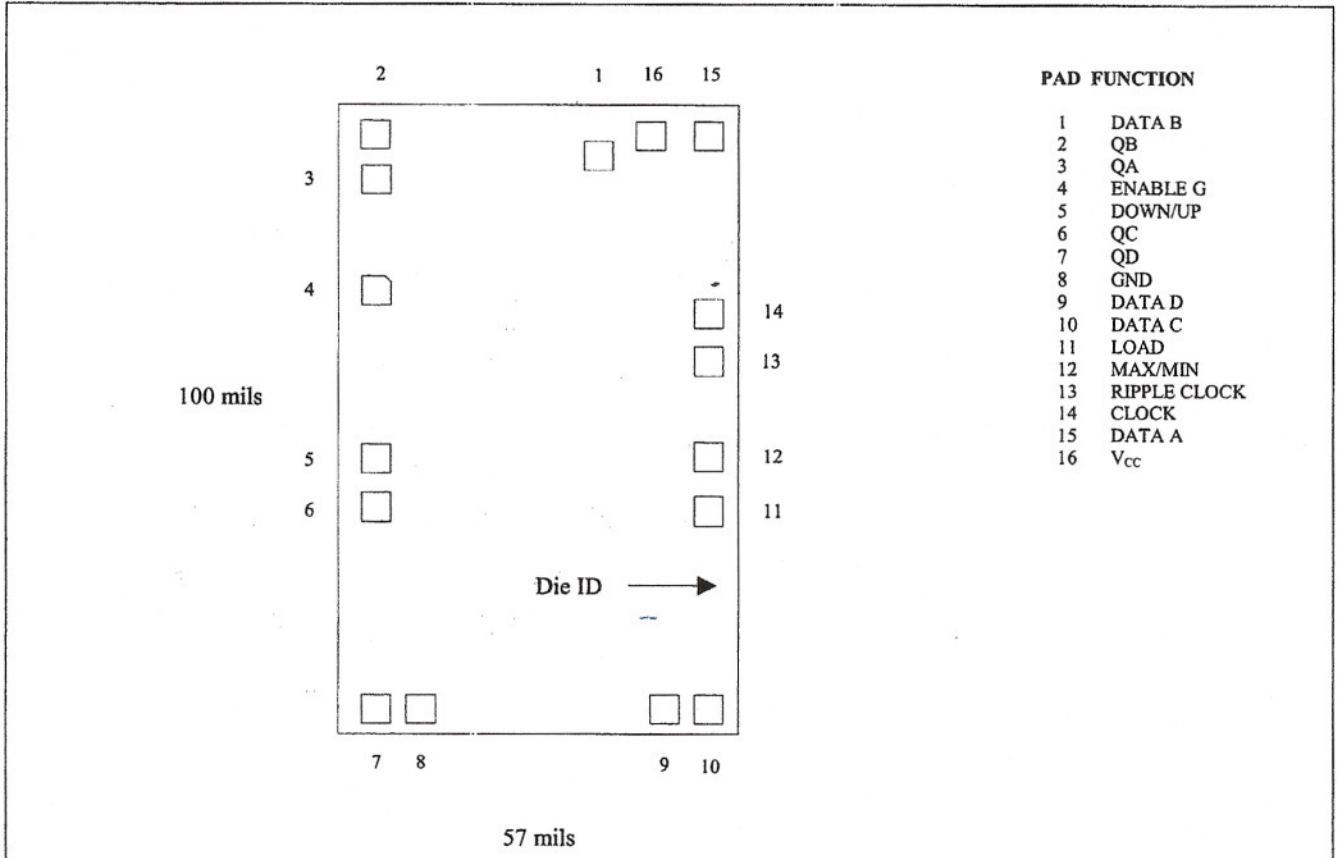




# Sierra Components, Inc.

2222 Park Place Building 3 Suite E • Minden, Nevada 89423

Phone: 775.783.4940 Fax: 775.783.4947



PAD FUNCTION	
1	DATA B
2	QB
3	QA
4	ENABLE G
5	DOWN/UP
6	QC
7	QD
8	GND
9	DATA D
10	DATA C
11	LOAD
12	MAX/MIN
13	RIPPLE CLOCK
14	CLOCK
15	DATA A
16	V <sub>CC</sub>

The information given is believed to be correct at the time of issue.  
 Please verify your requirements prior to commencement of any assembly process, as no liability for omission or error can be accepted.  
 Chip back potential is the level at which bulk silicon is maintained either by bond pad connection or in some cases the potential to which the chip back must be connected if stated above.

**Note: 1 mil = 0.001inch**

<p><u>APPROVED</u></p> <p>D Markham</p> <p>.....</p> <p>DATE: 24/08/01</p>	<p>54LS191</p> <p>FAIRCHILD</p>	<p><u>DIE INFORMATION</u></p> <p>DIMENSIONS (Mils): 57 x 100 x 14</p> <p>BOND PADS (Mils): 4 x 4</p> <p>MASK. REF:</p> <p>GEOMETRY:</p> <p>BACK POTENTIAL: GND</p>
<p><u>SERIAL NUMBER</u></p> <p>005345</p>		<p><u>METALLISATION</u></p> <p>TOP: Al</p> <p>BACK: Si</p>